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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number		
		Filing Date		
		First Named Inventor	Min-Hyung Cho	
		Art Unit		
		Examiner Name		
Sheet		of	Attorney Docket Number	51876P446

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
KT		"A VLSI Architecture for a High-Speed All-Digital Quadrature Modulator and Demodulator for Digital Radio Applications, H. Samueli, et al., IEEE Journal, vol., 8, No. 8, Oct. 1990, pages 1512-1519	
KT		"A Digital Quadrature Modulator With On-Chip D/A Converter", J. Vankka, IEEE Journal of Solid-state Circuits, Vol, 38, No. 10, Oct. 2003, pgs 1635-1642	

Examiner Signature	/Khai Tran/	Date Considered	02/02/2007
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Applicant's unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zatman (wlr) 08/11/2003.
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